

2/23

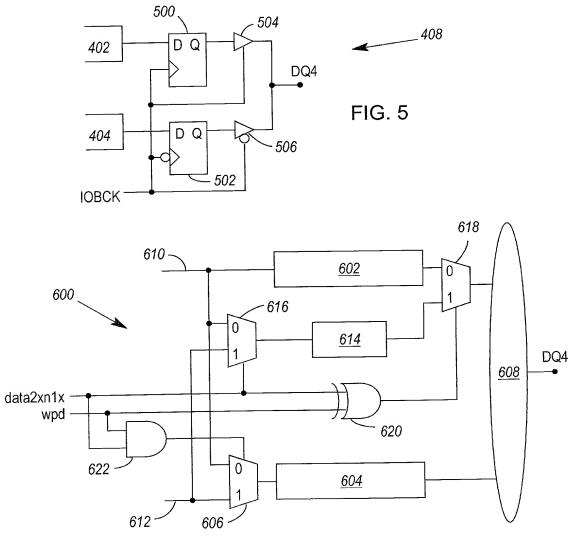


FIG. 6

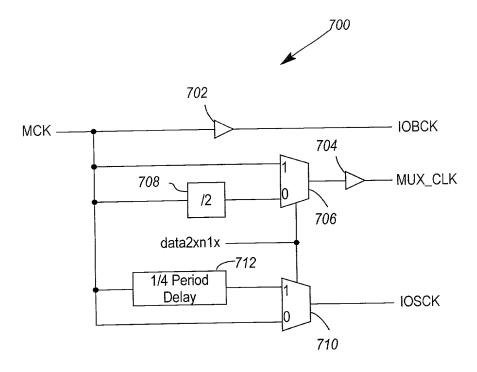


FIG. 7

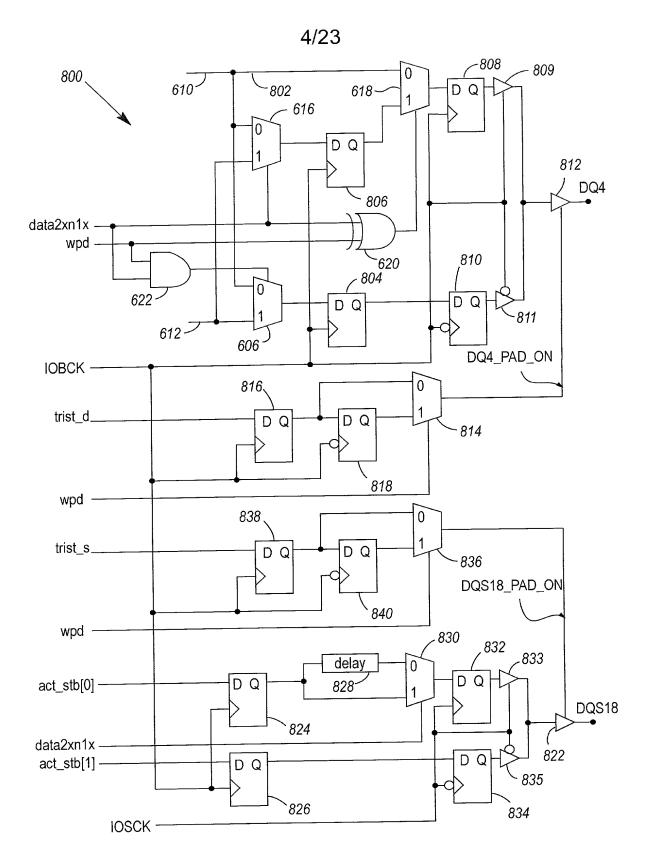
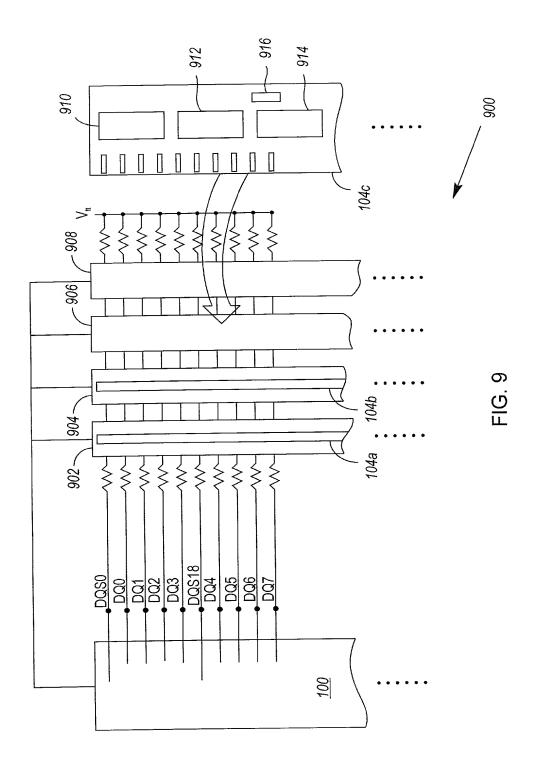
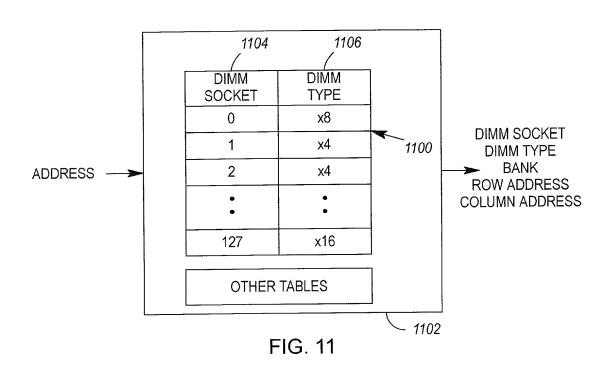


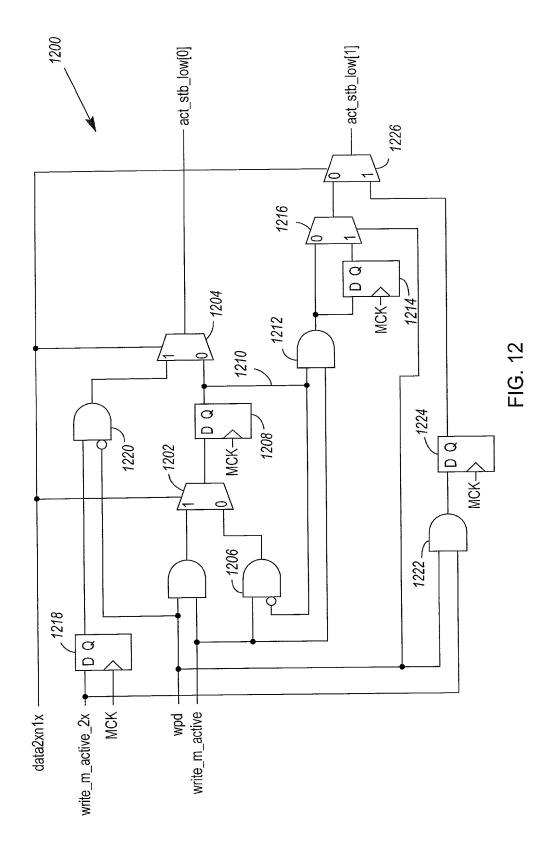
FIG. 8



STROBE PADS		DATA
x4 DIMM	x8 DIMM	PADS
0	0	3:0
18	0	7:4
1	1	11:8
19	1	15:12
2	2	19:16
20	2	23:20
3	3	27:24
21	3	31:28
•	•	•
•	•	•
•	•	•
16	16	131:128
34	16	135:132
17	17	139:136
35	17	143:140

FIG. 10





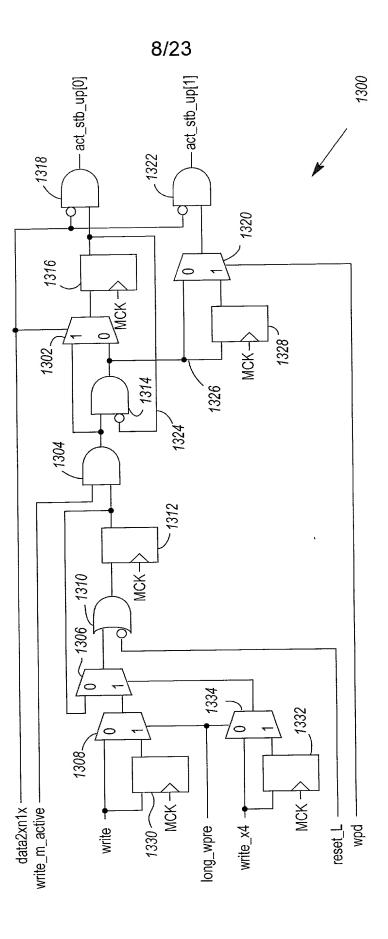


FIG. 13

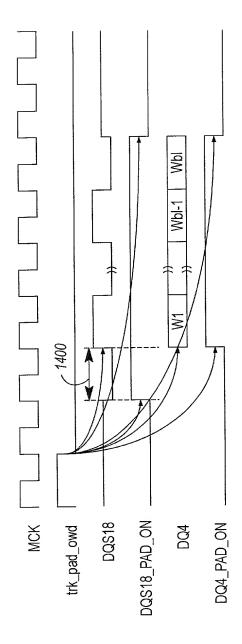


FIG. 14

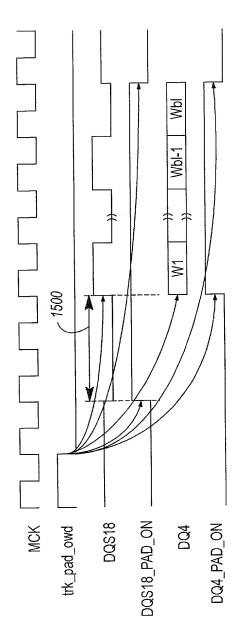


FIG. 15

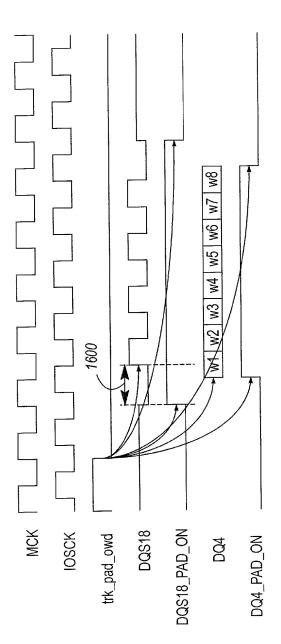


FIG. 16

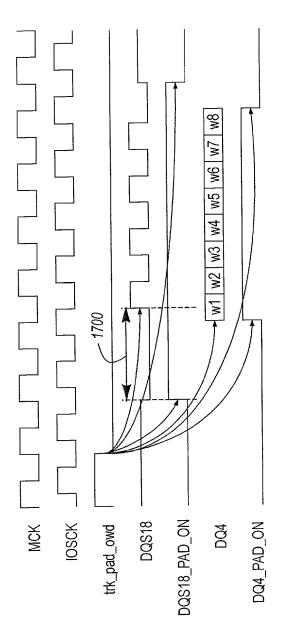
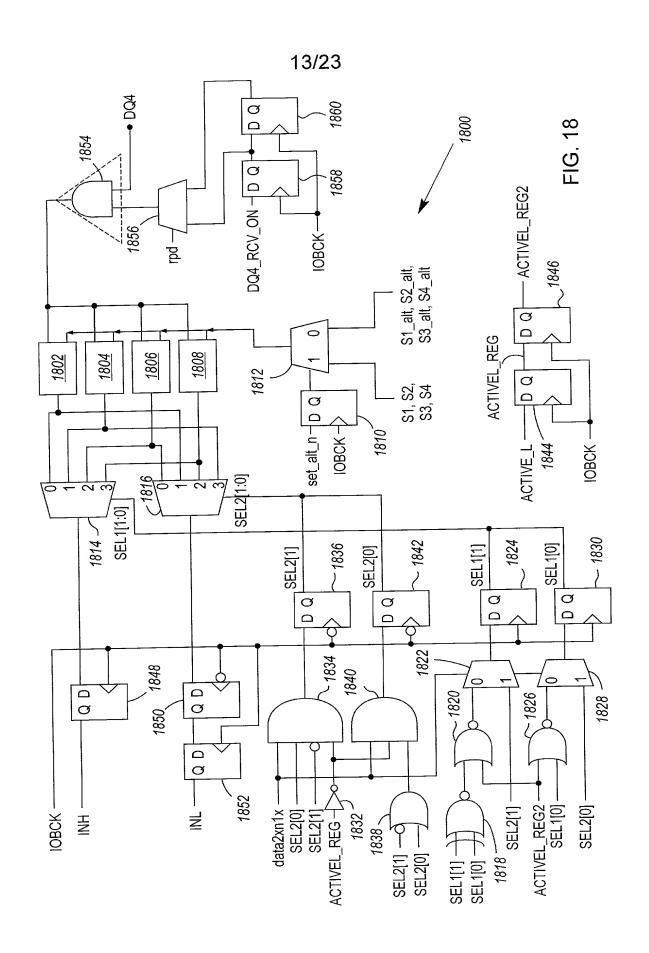
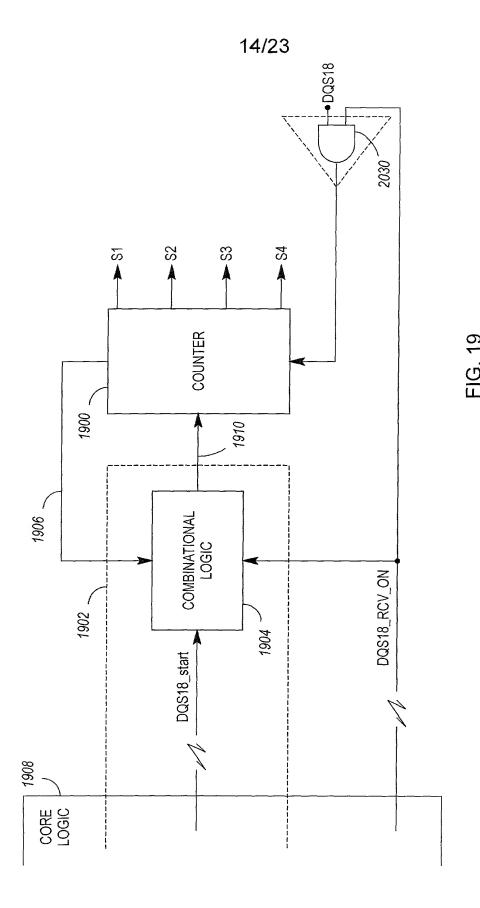
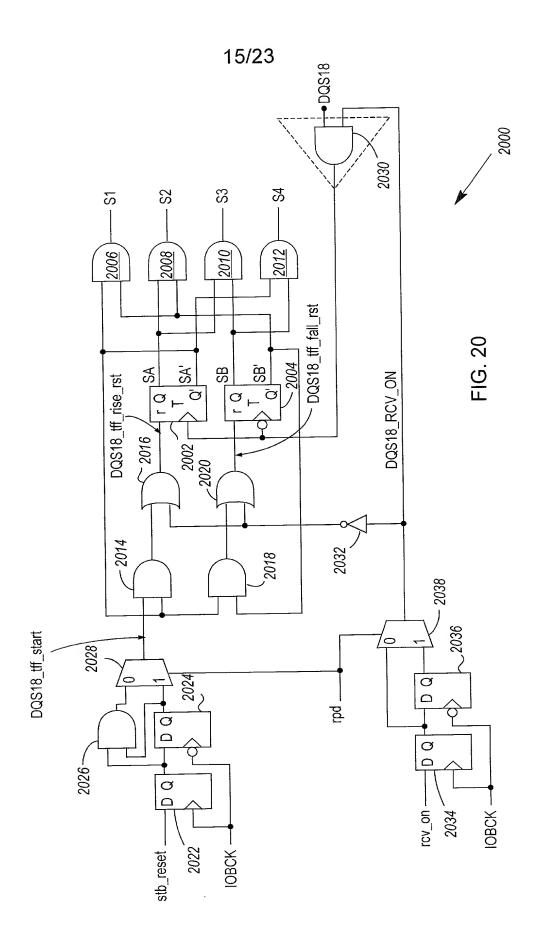


FIG. 17







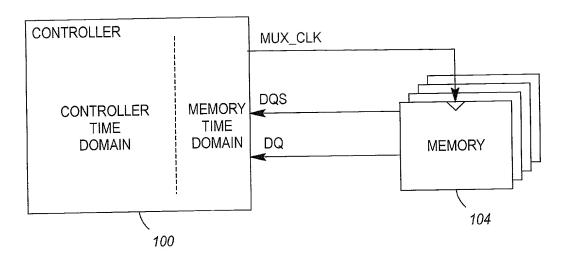


FIG. 21

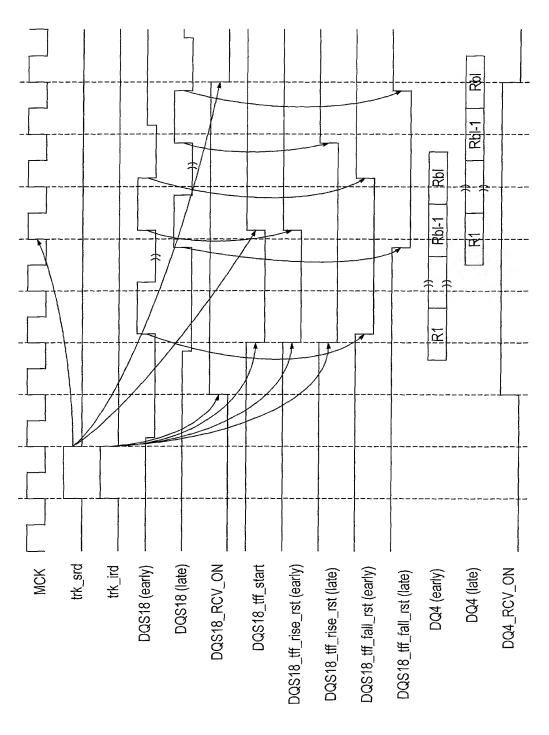
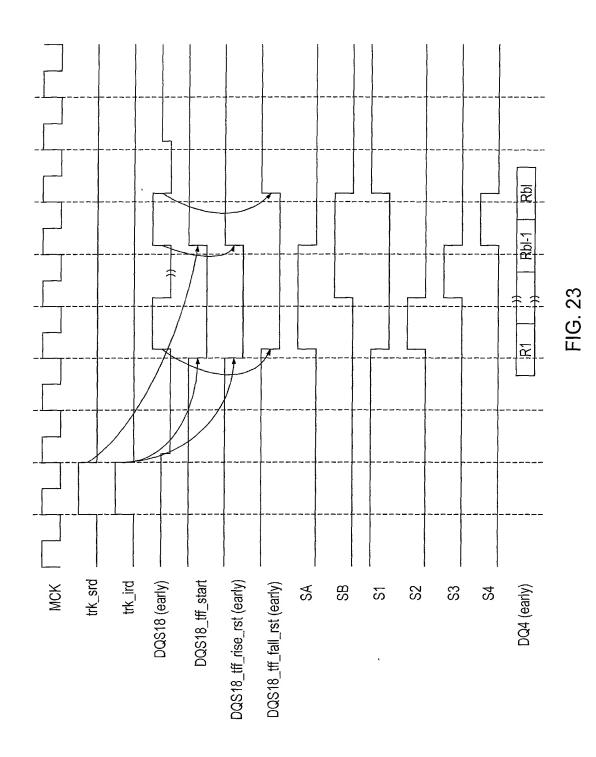
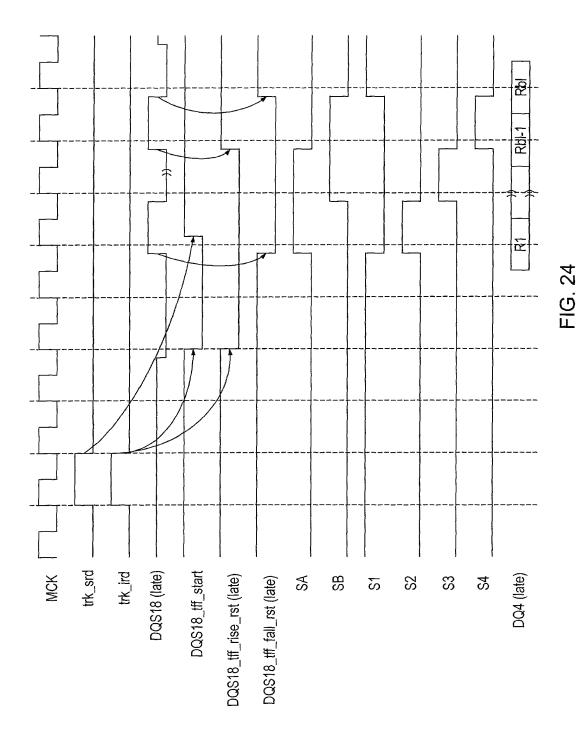
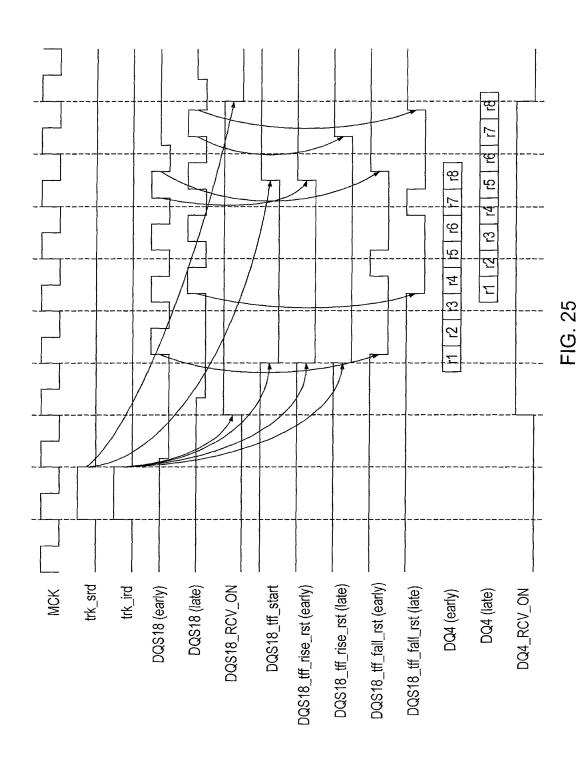
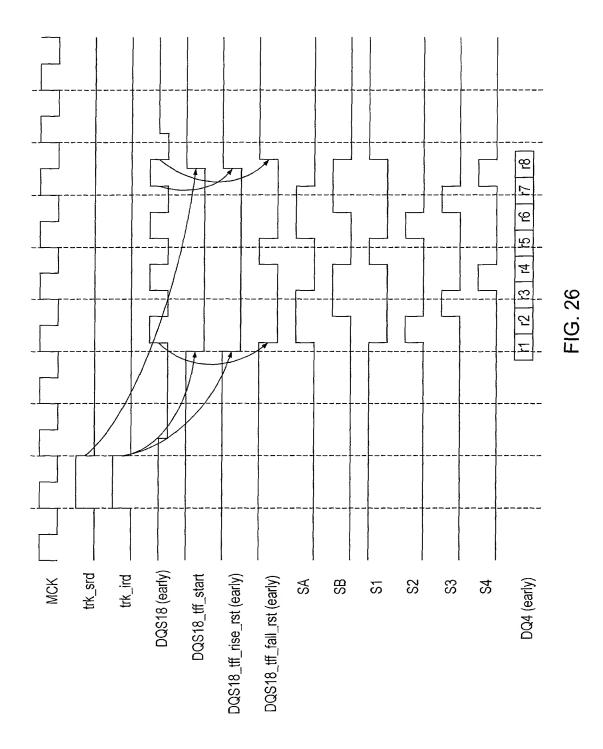


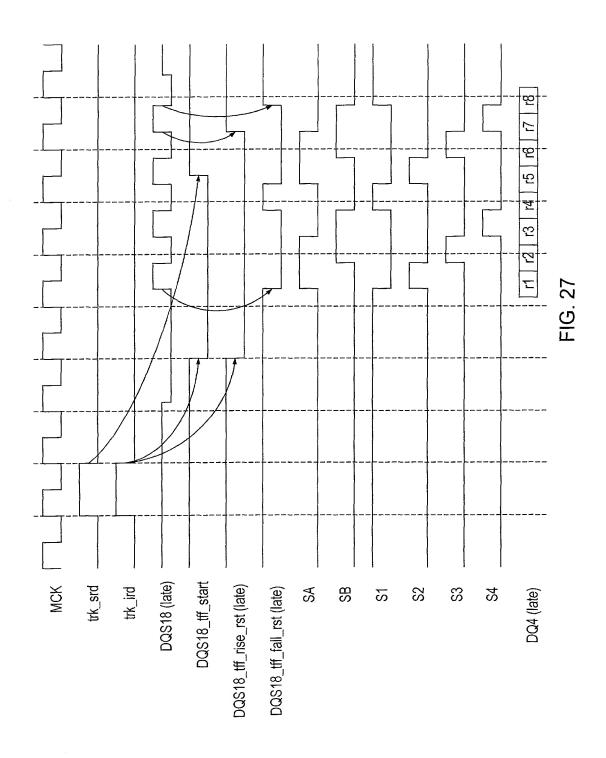
FIG. 22











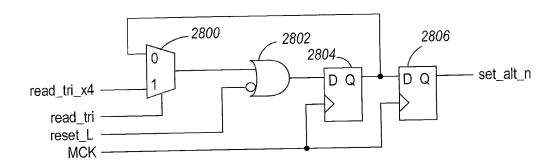


FIG. 28

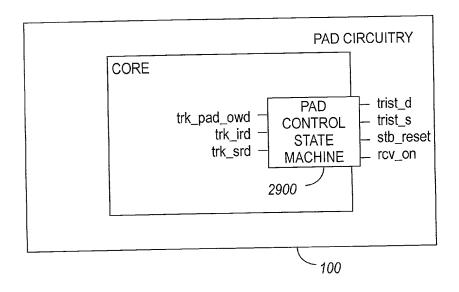


FIG. 29